

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	09992076
	Filing Date	2001-11-13
	First Named Inventor	Craig Nemecek
	Art Unit	2123
	Examiner Name	Jason Scott Proctor
	Attorney Docket Number	CD01210M

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	2	6014135	B1	2000-01-11	Fernandes, Antonio	Entire Document
	3	6052524	B1	2000-04-18	Pauna, Mark	Entire Document
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17	ANONYMOUS, "F/Port: Fast Parallel Port for the PC" Installation Manual" Release 7.1, circa 1997, available for download from http://www.fapo.com/fport.htm	<input type="checkbox"/>
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19	BURSKY, "FPGA Combines Multiple Interfaces and Logic," Electronic Design, Vol. 48 No. 20, pp.74-78 (2 October 2000)	<input type="checkbox"/>
20	"Webster's Third New International Dictionary", 1996, G. & C. Merriam Company pages 1328-1329	<input type="checkbox"/>
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22	GANAPATHY, GOPI, AND NARAYAN, RAM, AND JORDEN, GLEN, AND FERNANDEZ, DENZIL, AND WANG, MING, AND NISHIMURA, JIM: "Hardware Emulation for Functional Verification of K5", June 1996, 33rd Design Automation Conference Proceedings, 3-7 June 1996, pages 315-318	<input type="checkbox"/>
23	HINTZ ET AL.; "MICROCONTROLLERS", 1992, McGraw-Hill, pages 29-37	<input type="checkbox"/>
24	ITO, SERGIO AKIRA AND CARRO, LUIGI, "A Comparison of Microcontrollers Targeted to FPGA-Based Embedded Applications", September 2000, Proceedings of 13th Symposium on Integrated Circuits and Systems Design, 18-24 September 2000, pages 397-402	<input type="checkbox"/>
25	Julio Faura et al.: "A NOVEL MIXED SIGNAL PROGRAMMABLE DEVICE WITH ON-CHIP MICROPROCESSOR", 1997, IEEE 1997 Custom Integrated Circuits Conference, pages 103-568	<input type="checkbox"/>
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28	WALTERS, STEPHEN: "Computer-Aided Prototyping for ASIC-Based Systems", 1991, IEEE Design & Test of Computers; Volume 8, Issue 2, pages 4-10	<input type="checkbox"/>
29	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems., Cypress Microsystems, Inc. CMS10006A, July 3, 2001, pp. 1-25, cited by examiner	<input type="checkbox"/>

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30	"PSoC technology complete changes 8-bit MCU system design", Cypress Microsystems, Inc. retrieved from <http://www.archive.org/web/20040219/05250/http://cypress.mico.com-1...>, Feb. 19, 2001, pp. 1-21, cited by examiner	<input type="checkbox"/>
31	Specks et al., "A Mixed Digital-Analog 16B Microcontroller with 0.5MB Flash Memory, On-Chip Power Supply, Physical Network Interface, and 40V I/O for Automotive Single-Chip Mechatronics," IEEE, Feb. 9, 2000, pp. 346-349	<input type="checkbox"/>
32	Hsieh et al., "Modeling Micro-Controller Peripherals for High-Level Co-Simulation and Synthesis," IEEE, 1997, pp. 127-130...	<input type="checkbox"/>

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